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PATENT APPLICATION
DOCKET #1285-0080US

2661

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

PRASAD N. GOLLA et al.

Serial No.: 10/075,176

Filed: February 14, 2002

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Group Art Unit:

Examiner:

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Unassigned

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For: LOOK-UP TABLE ARBITRATION SYSTEM AND METHOD FOR A FAST
SWITCHING ELEMENT

Commissioner for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING BY FIRST CLASS MAIL:

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on . . . September 10, 2002 . . . by . . . Shreen K. Danamraj . . .

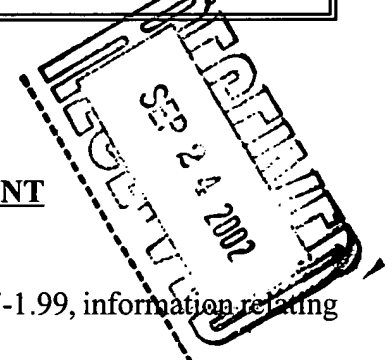
Signature

Shreen K. Danamraj

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 CFR §1.56 and in accordance with 37 CFR §§1.97-1.99, information relating to the above-identified application is hereby disclosed. Copies of each of the references listed on the attached Form PTO-1449 are enclosed herewith. Certain of the references may contain markings, underlinings or other notations. These markings are not intended and should not be construed as drawing the Examiner's attention either to selected parts or away from other parts of the references,



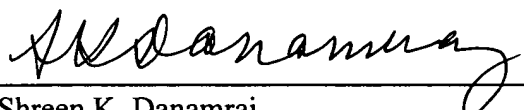
**PATENT APPLICATION
DOCKET #1285-0080US**

but such markings were either present on the copies of the references obtained by applicant, or were made thereon during the study of the references by applicant and/or its attorneys.

The submission of any document herewith is not considered as an admission that such document constitutes prior art against the claims of the present application. No rights are waived hereby to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a prima facie prior art reference against the claims of the present application.

Respectfully submitted,

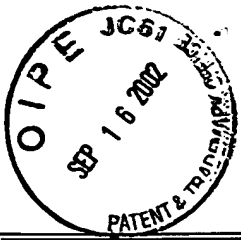
Date: September 10, 2002



Shreen K. Danamraj
Registration No. 41,696

DANAMRAJ & YOUST, P.C.
12900 Preston Road, Suite 1200, LB-15
Dallas, Texas 75230-1328

(972) 720-1202

Sheet 2 of 2

Form PTO-1449 Modified List of Patents and Publications Cited by Applicant (Use several sheets if necessary) U.S. Patent Department of Commerce Patent and Trademark Office		Docket No.: 1285-0080US	Serial No.: 10/075,176
		Applicant: Prasad N. Golla et al.	
		Filing Date: February 14, 2002	Group: Unassigned
OTHER DOCUMENTS			
Examiner Initial		Author, Title, Date, Pertinent Pages, Etc.	
	AA	TIMA Lab. Research Reports; "Modeling and design of asynchronous priority arbiters for on-chip communication systems"; J.B. Rigaud, J. Quartana, L. Fesquet and M. Renaudin; 7 pages; 2001.	
	AB		
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EXAMINER:		DATE CONSIDERED:	

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